

**NATIONAL BOARD OF ACCREDITATION**

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

<b>Program Name</b> : ELECTRONICS AND COMMUNICATION ENGINEERING	<b>Discipline</b> : Engineering & Technology
<b>Level</b> : Under Graduate	<b>Tier</b> : 1
<b>Application No</b> : 11700	<b>Date of Submission</b> : 13-03-2026

**PART A- Profile of the Institute**

<b>A1.Name of the Institute</b> : P. A. COLLEGE OF ENGINEERING AND TECHNOLOGY	
Year of Establishment : 2008	Location of the Institute: Pollachi
<b>A2. Institute Address</b> :PALLADAM ROAD, POLLACHI - 642 002.	
City:Coimbatore	State:Tamil Nadu
Pin Code:642002	Website:www.pacolleges.org
Email:pacollege@yahoo.com	Phone No(with STD Code):04259-221386
<b>A3. Name and Address of the Affiliating University (if any)</b> :	
Name of the University : ANNA UNIVERSITY	City: Chennai
State : Tamil Nadu	Pin Code: 600025
<b>A4. Type of the Institution</b> : Autonomous CAY(2019-20)	
<b>A5. Ownership Status</b> : Self financing	

**A6. Details of all Programs being Offered by the Institution:**

- No. of UG programs: 6
- No. of PG programs: 2

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Engineering & Technology	UG	Artificial Intelligence and Data Science	2022	--	Computer Science and Engineering
2	Engineering & Technology	PG	Computer Science and Engineering	2011	--	Computer Science and Engineering
3	Engineering & Technology	UG	Computer Science and Engineering	2008	--	Computer Science and Engineering
4	Engineering & Technology	UG	Electrical and Electronics Engineering	2008	--	Electrical and Electronics Engineering
5	Engineering & Technology	UG	ELECTRONICS AND COMMUNICATION ENGINEERING	2008	--	Electronics and Communication Engineering
6	Engineering & Technology	UG	Information Technology	2022	--	Computer Science and Engineering
7	Engineering & Technology	UG	Mechanical Engineering	2009	--	Mechanical Engineering

8	Engineering & Technology	PG	Power Electronics & Drives	2012	--	Electrical and Electronics Engineering
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**A7. Programs to be considered for Accreditation vide this Application:**

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Computer Science and Engineering	Yes	Computer Science and Engineering	UG
Electronics and Communication Engineering	No	ELECTRONICS AND COMMUNICATION ENGINEERING	UG
Electrical and Electronics Engineering	No	Electrical and Electronics Engineering	UG
Mechanical Engineering	No	Mechanical Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.  
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record
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**PART-B: Program information****B1. Provide the Required Information for the Program Applied For:**

Table No. B1: Program details.

## A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY APPROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PRC DUF
1	ELECTRONICS AND COMMUNICATION ENGINEERING	UG	2008 / --	60	Yes	2025	90	2025	F.No. Southern/1-44643084712/2025/EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	3	4

**Sanctioned Intake for Last Five Years for the ELECTRONICS AND COMMUNICATION ENGINEERING**

Academic Year	Sanctioned Intake
2025-26	90
2024-25	120
2023-24	120
2022-23	120
2021-22	120
2020-21	120

## List of the Allied Departments/Cluster and Programs:

**B2. Detail of Head of the Department for the program under consideration:**

A. Name of the HoD :

Dr. L. MURALI

B. Nature of appointment:	Regular
C. Qualification:	Ph.D

**B3. Program Details**

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	90	120	120	120	120	120	120
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	89	84	88	120	85	39	70
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	7	3	9	22	24	2
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	0	0	2	0	0	0	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	89	91	93	129	107	63	72

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

**B4. Enrolment Ratio in the First Year**

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	90	89	0	98.89
2024-25 (CAYm1)	120	84	0	70.00
2023-24 (CAYm2)	120	88	2	75.00

Average [ (ER1 + ER2 + ER3) / 3 ] = 81.30≡ 17.00

**B5. Success Rate of the Students in the Stipulated Period of the Program**

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	142.00	144.00	122.00
B=No. of students who graduated from the program in the stipulated course duration	98.00	43.00	63.00
Success Rate (SR)= (B/A) * 100	69.01	29.86	51.64

Average SR of three batches ((SR\_1+ SR\_2+ SR\_3)/3): 50.17

**B6. Academic Performance of the First-Year Students of the Program**

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1( 2024-25 )	CAYm2( 2023-24 )	CAYm3 ( 2022-23 )
Mean of CGPA or mean percentage of all successful students(X)	7.75	7.73	7.71
Y=Total no. of successful students	81.00	87.00	118.00
Z=Total no. of students appeared in the examination	84.00	90.00	120.00
API [X*(Y/Z)]	7.47	7.47	7.58

Average API [ (AP1+AP2+AP3)/3 ] : 7.51

**B7: Academic Performance of the Second Year Students of the Program**

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 ( 2024-25 )	CAYm2 ( 2023-24 )	CAYm3 ( 2022-23 )
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2nd year/10)	7.72	7.72	7.68
Y=Total no. of successful students	89.00	126.00	102.00
Z=Total no. of students appeared in the examination	90.00	127.00	103.00
API [ X * (Y/Z) ]	7.63	7.66	7.61

Average API [ (AP1 + AP2 + AP3)/3 ] : 7.63

**B8. Academic Performance of the Third Year Students of the Program**

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.75	7.71	7.97
Y=Total no. of successful students	126.00	102.00	54.00
Z=Total no. of students appeared in the examination	126.00	102.00	56.00
API [ X*(Y/Z) ]:	7.75	7.71	7.69

Average API [ (AP1 + AP2 + AP3)/3 ] : 7.72

**B9. Placement, Higher Studies, and Entrepreneurship**

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	142.00	144.00	122.00
X=No. of students placed	74.00	46.00	57.00
Y=No. of students admitted to higher studies	5.00	2.00	1.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = ((X + Y + Z)/FS) * 100):	55.63	33.33	47.54

Average Placement Index = (P\_1 + P\_2 + P\_3)/3: 45.50 Placement Index Points:

**PART C: Faculty Details in Department and Allied Departments**  
**(Data to be filled in for the Department and Allied Departments)**

## C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr. L. MURALI	XXXXXXXX98D	Ph.D	Anna University	VLSI Signal Processing	17/06/2016	9.8	Associate Professor	Professor	02/01/2023	Regular	Yes		Yes
2	Dr. R. P. MEENAKSHI SUNDARI	XXXXXXXX55P	Ph.D	Anna University	VLSI Design	16/12/2016	9.2	Professor	Professor	16/12/2016	Regular	Yes		No
3	Dr. M. YUVARAJA	XXXXXXXX57G	Ph.D	Anna University	Networking	26/05/2010	15.9	Assistant Professor	Professor	02/06/2025	Regular	Yes		No
4	Dr. K. S. NEELU KUMARI	XXXXXXXX20F	Ph.D	Anna University	Testing of VLSI	05/05/2014	11.10	Assistant Professor	Associate Professor	02/12/2019	Regular	Yes		No
5	Dr. D. KUMAR	XXXXXXXX15Q	Ph.D	Anna University	Communication systems	11/05/2011	14.10	Assistant Professor	Associate Professor	02/12/2019	Regular	Yes		No
6	Mr. B. SARANRAJ	XXXXXXXX47J	M.E.	Anna University	Applied Electronics	03/06/2013	12.9	Assistant Professor	Assistant Professor		Regular	Yes		No
7	Mr. G. ARAVINDH	XXXXXXXX34J	M.E.	Anna University	VLSI Design	02/03/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
8	Mrs. K. POORNIMADEVI	XXXXXXXX69A	M.E.	Anna University	Applied Electronics	06/03/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
9	Mrs. V. MADHUBASHINI	XXXXXXXX24F	M.E.	Anna University	VLSI Design	06/03/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
10	Ms. K. DEENU	XXXXXXXX44L	M.E.	Anna University	Applied Electronics	26/09/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
11	Mrs. S. KAVIYA	XXXXXXXX14E	M.E.	Anna University	VLSI Design	02/01/2024	2.2	Assistant Professor	Assistant Professor		Regular	Yes		No
12	Mrs. A. ANDRINE DINOLA	XXXXXXXX75K	M.E.	Anna University	Applied Electronics	02/01/2024	2.2	Assistant Professor	Assistant Professor		Regular	Yes		No
13	Mrs. K. SANGEETHA	XXXXXXXX47P	M.E.	Anna University	Applied Electronics	13/09/2023	2.5	Assistant Professor	Assistant Professor		Regular	Yes		No
14	Mr. T. GOWTHAM	XXXXXXXX52K	M.E.	Anna University	Applied Electronics	09/01/2024	2.2	Assistant Professor	Assistant Professor		Regular	Yes		No
15	Ms. M. SOWMYA	XXXXXXXX18L	M.E.	Anna University	Applied Electronics	01/07/2024	1.8	Assistant Professor	Assistant Professor		Regular	Yes		No
16	Mr. R. VISHNU VARDHAN	XXXXXXXX42K	M.E.	Anna University	VLSI Design	22/12/2015	10.2	Assistant Professor	Assistant Professor		Regular	Yes		No

17	Mr. S. SRI RAM SURYA	XXXXXXX00G	M.E.	Anna University	VLSI Design	01/07/2024	1.8	Assistant Professor	Assistant Professor		Regular	Yes		No
18	Mrs. P. DURGGASHRE	XXXXXXX24R	M.E.	Anna University	Communication systems	01/07/2024	1.8	Assistant Professor	Assistant Professor		Regular	Yes		No
19	Mr. N. SENATHIPATHI	XXXXXXX38K	M.E.	Anna University	VLSI Design	02/05/2011	14.10	Assistant Professor	Assistant Professor		Regular	Yes		No
20	Mr. S. SENTHAMILARASU	XXXXXXX81Q	M.E.	Anna University	Applied Electronics	16/05/2012	13.9	Assistant Professor	Assistant Professor		Regular	Yes		No
21	Mrs. M. MADHU MALIN	XXXXXXX89D	M.E.	Anna University	Communication systems	13/05/2011	14.10	Assistant Professor	Assistant Professor		Regular	Yes		No
22	Mr.S.NAGAKUMARARAJ	XXXXXXX82P	M.E.	Anna University	VLSI Design	23/09/2022	2.9	Assistant Professor	Assistant Professor		Regular	No	30/06/2025	No
23	Mr. K. Jaikumar	XXXXXXX70H	M.E.	Anna University	EMBEDDED SYSTEMS	03/06/2013	11	Assistant Professor	Assistant Professor		Regular	No	28/06/2024	No
24	Mr. N. Sarveswaran	XXXXXXX29M	M.E.	Anna University	EMBEDDED SYSTEMS	03/06/2013	10.5	Assistant Professor	Assistant Professor		Regular	No	20/11/2023	No
25	Mr. R. Ranjith Kumar	XXXXXXX06B	M.E.	Anna University	Applied Electronics	03/06/2013	10.6	Assistant Professor	Assistant Professor		Regular	No	28/12/2023	No
26	Mrs. R. Ambika	XXXXXXX30E	M.E.	Anna University	COMPUTER AND COMMUNICATION ENGINEERING	02/09/2020	3.9	Assistant Professor	Assistant Professor		Regular	No	28/06/2024	No
27	Mr. M. Giriprakash	XXXXXXX57A	M.E.	Anna University	VLSI Design	02/09/2020	3.9	Assistant Professor	Assistant Professor		Regular	No	28/06/2024	No
28	Mrs. S. Saraswathi	XXXXXXX07E	M.E.	Anna University	VLSI Design	01/12/2020	3.6	Assistant Professor	Assistant Professor		Regular	No	26/06/2024	No
29	Mr. R. Yaswanthraj	XXXXXXX21L	M.E.	Anna University	VLSI Design	04/09/2020	3.9	Assistant Professor	Assistant Professor		Regular	No	28/06/2024	No
30	Mrs. P. Premalatha	XXXXXXX71N	M.E.	Anna University	VLSI Design	01/02/2022	2.4	Assistant Professor	Assistant Professor		Regular	No	26/06/2024	No
31	Mrs. G. Sakana	XXXXXXX29B	M.E.	Anna University	VLSI Design	01/02/2022	2.4	Assistant Professor	Assistant Professor		Regular	No	26/06/2024	No
32	Mrs. N. Nandhini	XXXXXXX52M	M.E.	Anna University	VLSI Design	01/02/2022	2.4	Assistant Professor	Assistant Professor		Regular	No	26/06/2024	No
33	Mr. J. Jeyakumar	XXXXXXX58M	M.E.	Anna University	COMPUTER AND COMMUNICATION ENGINEERING	26/05/2022	2.1	Assistant Professor	Assistant Professor		Regular	No	28/06/2024	No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

**C2. Student-Faculty Ratio (SFR)**

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

**B**= No. of Students in UG 2nd year (ST)

**C**= No. of Students in UG 3rd year (ST)

**D**= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

**A**= No. of Students in PG 1st year

**B**= No. of Students in PG 2nd year

Student Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

**No. of students (ST)**=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department0

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	127	123	129
UG1.C	123	129	132
UG1.D	129	132	132
<b>UG1: ELECTRONICS AND COMMUNICATION ENGINEERING</b>	<b>379</b>	<b>384</b>	<b>393</b>
DS=Total no. of students in all UG and PG programs in the Department	379	384	393
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	<b>S1= 379</b>	<b>S2= 384</b>	<b>S3= 393</b>
DF=Total no. of faculty members in the Department	21	22	24
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	<b>F1= 21</b>	<b>F2= 22</b>	<b>F3= 24</b>
FF=The faculty members in F who have a 100% teaching load in the first-year courses	1	1	1
Student Faculty Ratio (SFR)=S/(F-FF)	<b>SFR1= 18.95</b>	<b>SFR2= 18.29</b>	<b>SFR3= 17.09</b>
Average SFR for 3 years	<b>SFR= 18.11</b>		

### C3. Faculty Qualification

- Faculty qualification index (FQI) =  $2.5 * [(10X + 4Y)/RF]$  where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 * [(10X + 4Y) / RF]$
2025-26(CAY)	5	16	18.00	15.83
2024-25(CAYm1)	5	17	19.00	15.53
2023-24(CAYm2)	4	20	19.00	15.79

**C4. Faculty Cadre Proportion**

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required =  $1/9 \times$  No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:.
- RF2= No. of Associate Professors required =  $2/9 \times$  No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- RF3= No. of Assistant Professors required =  $6/9 \times$  No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	2.00	3.00	4.00	2.00	12.00	16.00
2024-25	2.00	2.00	4.00	3.00	12.00	17.00
2023-24	2.00	2.00	4.00	2.00	13.00	20.00
Average	RF1=2.00	AF1=2.33	RF2=4.00	AF2=2.33	RF2=12.33	AF2=17.67

**C5. Visiting/Adjunct Faculty/Professor of Practice**

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

**(CAYm1)**

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	MR. R V Yaswantraj	proprietor	Jayam Packaging	Embedded Systems	30.00
2	MR. R V Yaswantraj	proprietor	Jayam Packaging	Embedded Systems Lab	30.00

**(CAYm2)**

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr.ARULPRAKASAM C R	Rtd Executive Engineer	PWD (Govt. TN)	Environmental Science and Engineering	45.00
2	Mr.K Robert	Rtd Executive Engineer	PWD (Govt. TN)	Engineering Graphics	50.00

**(CAYm3)**

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr.ARULPRAKASAM C R	Rtd Executive Engineer	PWD (Govt. TN)	Environmental Science and Engineering	45.00
2	Mr.K Robert	Rtd Executive Engineer	PWD (Govt. TN)	Engineering Graphics	50.00

**C6. Academic Research**

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	21	15	20



2	No. of peer reviewed conference papers published	14	11	8
3	No. of books/book chapters published	1	1	4

**C7. Sponsored Research Project**

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. R. P. Meenaashi Sundhari	P. K. Nivethitha M. Pravina M.Priyadarshini A. R. Santhiya P. Sibiksha	ECE	Reckon Coco	MSME	6Months	13.30
Mr. G. Aravind	S.sriharsh K.Vasanthakumar M.Sathoshkumar S. Suthirbarath D. Umashankar	ECE	Power Extraction from Renewable Soil and Waste Water	MSME	6Months	10.00
						Amount received (Rs.):23.30

(CAYm2)

(CAYm3)

**Total Amount (Lacs) Received for the Past 3 Years: 23.30****Note\*:**

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

**C8. Consultancy Work**

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Mr.R.Vishnu Vardhan	Mr.S.Sriramsuriya	ECE	Real-Time Monitoring and Control of Industrial Parameters	Bluro Technology Solutions	4 Months	0.50
Mr.B.Saranraj	Mr.G.Aravind	ECE	form filling and sealing machine grocery	Jayam Packaging	4 Months	3.00
						Amount received (Rs.):3.50

## (CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Mrs. M. Madhumalini	Mr. S. Senthilarasu & Mr. R. Vishnu Vardhan	ECE	Smart Amanuesis system	Wizaard systems	5 Months	0.37
Dr.L.Murali	Mr.G.Aravindh	ECE	An Efficient Animatronic Robotic Gesture Recognition Human Machine Interaction Application	VI Microsystems	6 Months	1.10
						Amount received (Rs.):1.47

## (CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. L. Murali	Dr.K.S.Neelukumari, & Mr.B.Saranraj.	ECE	Web-Based Energy Monitoring System and Tariff Calculation for Residential Electricity Consumption	Tectzo Solutions Private Limited,	3 Months	0.50
Dr. L. Murali	Mr.S.Nagakumararaj & Dr.R.P.Meenaakshi Sundhari	ECE	Automatic Rewinder machine	Jayam Packaging	6 Months	2.10
						Amount received (Rs.):2.60

**Total amount (Lacs) received for the past 3 years: 7.57**

**Note\*:**

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

**C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work**

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

## (CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
K. S. Neelukumari, L. Murali	A Predictive Machine Learning Analysis for Stroke Prediction	6 months	0.14	0.14	Published in Scopus indexed IEEE Conference
L. Murali, M. Yuvaraja	Alzheimer's Disease Detection and Classification in Biomarkers Using Deep Learning	6 months	0.14	0.14	Published in Scopus indexed IEEE Conference
R. Vishnu Vardhan,G Aravindh	Comparative Evaluation of Diabetic Retinopathy Diagnosis Using Fundus Imaging Technique	6 months	0.14	0.14	Published in Scopus indexed IEEE Conference
			Amount received (Rs.): 0.42		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
K.S. Neelu Kumari & S. Nagakumararaj S	Implementation of Ripple Carry Adder Design for Optimal VLSI	6 months	0.20	0.20	Published in Scopus indexed IEEE Conference
L. Murali, R, Vishnu Vardhan	Automatic Sign Language Recognition Using Convolutional Neural Networks	6 months	0.14	0.14	Published in Scopus indexed IEEE Conference
G. Aravindh, L. Murali	A Visual Cryptographic Scheme for Colour QR Codes in Defence	6 months	0.14	0.14	Published in Scopus indexed IEEE Conference
			Amount received (Rs.): 0.48		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Mr. G. Aravindh & Mr. R. Vishnu Varadhan	Design and Implementation of a Autonomous Drone with Monitoring and Predictive Maintenance	1 year	1.10	1.10	Implemented predictive maintenance to reduce unexpected failures in practical deployments
			Amount received (Rs.): 1.10		

Total amount (Lacs) received for the past 3 years : 2.00

## PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

## D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Analog/Digital circuits Laboratory	30	□ CRO/DSO □ FunctionGenerator □ Dual regulated power supplies □ IC Trainer kit □ Digital Multimeter □ IC Tester □ Digital Trainer	12 Hours/week	Mrs. Kiruthika	Lab Technician	B.Sc
2	Communication Systems Laboratory	30	□ CRO □ Function Generator □ Trainer kits for AM & FM □ Trainer kits for signal sampling □ Trainer kits for TDM □ Trainer kits for PCM	12 Hours/week	Mrs. Ranjitha	Lab Technician	B.Sc
3	Linear Integrated Circuits Laboratory	30	□ Analog Trainer kit □ CRO □ DSO □ Digital Trainer kit □ Function Generators □ Power Supply Unit □ IC Tester	18 Hours/week	Mrs. Ranjitha	Lab Technician	B.Sc
4	Microprocessors and Microcontrollers Laboratory	30	□ 8086 Microprocessor Trainer kit □ 8051 Microcontroller Trainer kit □ Motor control □ Keyboard and Display Interface □ ADC and DAC	12 Hours/week	Mr. Muruganantham	Lab Technician	B.E

5	Embedded systems and Networks Laboratory	30	□ PCs, □ LAN trainer kit □ ARM Development Board	18 Hours/week	Mr. Muruganatham	Lab Technician	B.E
6	VLSI Design and Digital Signal Processing Laboratory	30	□ DSP Processors, □ MATLAB, □ Spartan 3E FPGA Trainer Kits □ Xilinx Software □ Mentor Graphics □ Cadence □ Tepper □ CRO □	12 Hours/week	Mr. Muruganatham	Lab Technician	B.E
7	Optical and Microwave Laboratory	30	□ Reflex klystron □ Optical trainer kit □ PIN modulator □ Wave guides □ Horn antenna □	12 Hours/week	Mrs. Kiruthika	Lab Technician	B.Sc
8	Project Laboratory	30	□ PC with Open Source Software	24 Hours/week	Mr. Muruganatham	Lab Technician	B.E

## D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Analog/Digital circuits Laboratory	<ul style="list-style-type: none"> <li>• Display of component handling guidelines.</li> <li>• Switch OFF the power supply before making or changing circuit connections.</li> <li>• Check all wiring and component values carefully to avoid short circuits.</li> <li>• Use insulated wires, probes, and properly grounded equipment for protection.</li> <li>• Never touch live circuits or exposed conductors while the power is ON.</li> <li>• Observe correct polarity and ratings of components such as ICs, diodes, and capacitors.</li> <li>• Handle laboratory instruments like CRO, multimeter, and power supply carefully and use proper measurement ranges.</li> <li>• Report faulty equipment immediately and know the location of emergency power switches and fire extinguishers.</li> </ul>
2	Communication Systems Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF all communication equipment before making or changing connections.</li> <li>• Check transmitter, receiver, and power supply connections properly before switching ON the circuit.</li> <li>• Use instruments like CRO, spectrum analyzer, and signal generator carefully and within specified limits.</li> <li>• Avoid touching live terminals, RF connectors, and exposed wires during operation.</li> <li>• Ensure proper grounding of all electronic communication equipment to prevent electric shock and signal interference.</li> <li>• Handle antennas, optical fibers, and sensitive communication modules carefully to avoid damage.</li> <li>• Keep the laboratory neat and immediately report overheating, sparks, damaged cables, or faulty instruments to the lab instructor.</li> </ul>
3	Linear Integrated Circuits Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the power supply before inserting or removing ICs from the circuit.</li> <li>• Check the pin configuration and polarity of ICs carefully before connections.</li> <li>• Avoid applying excessive voltage or current beyond the IC ratings.</li> <li>• Handle ICs with care and use anti-static precautions to prevent damage.</li> <li>• Verify all circuit connections properly to avoid short circuits and overheating.</li> <li>• Use CRO, function generator, and multimeter correctly with proper measurement ranges.</li> <li>• Report faulty ICs, damaged components, or abnormal heating immediately to the lab instructor.</li> </ul>
4	Microprocessors and Microcontrollers Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the trainer kit or power supply before making circuit connections.</li> <li>• Verify all data bus, address bus, and interfacing connections carefully before powering ON.</li> <li>• Handle microprocessors, microcontrollers, and ICs carefully to avoid static damage.</li> <li>• Use the correct power supply voltage and avoid overloading the trainer kit.</li> <li>• Do not insert or remove ICs while the circuit is energized.</li> <li>• Use programming and debugging tools properly to prevent software or hardware errors.</li> <li>• Report loose connections, overheating, damaged components, or faulty trainer kits immediately to the lab instructor.</li> </ul>
5	Embedded systems and Networks Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the trainer kit or power supply before making circuit connections.</li> <li>• Verify all data bus, address bus, and interfacing connections carefully before powering ON.</li> <li>• Handle microprocessors, microcontrollers, and ICs carefully to avoid static damage.</li> <li>• Use the correct power supply voltage and avoid overloading the trainer kit.</li> <li>• Do not insert or remove ICs while the circuit is energized.</li> <li>• Use programming and debugging tools properly to prevent software or hardware errors.</li> <li>• Report loose connections, overheating, damaged components, or faulty trainer kits immediately to the lab instructor.</li> </ul>
6	VLSI Design and Digital Signal Processing Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the trainer kit or power supply before making circuit connections.</li> <li>• Verify all data bus, address bus, and interfacing connections carefully before powering ON.</li> <li>• Handle microprocessors, microcontrollers, and ICs carefully to avoid static damage.</li> <li>• Use the correct power supply voltage and avoid overloading the trainer kit.</li> <li>• Do not insert or remove ICs while the circuit is energized.</li> <li>• Use programming and debugging tools properly to prevent software or hardware errors.</li> <li>• Report loose connections, overheating, damaged components, or faulty trainer kits immediately to the lab instructor.</li> </ul>

7	Optical and Microwave Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the trainer kit or power supply before making circuit connections.</li> <li>• Verify all data bus, address bus, and interfacing connections carefully before powering ON.</li> <li>• Handle microprocessors, microcontrollers, and ICs carefully to avoid static damage.</li> <li>• Use the correct power supply voltage and avoid overloading the trainer kit.</li> <li>• Do not insert or remove ICs while the circuit is energized.</li> <li>• Use programming and debugging tools properly to prevent software or hardware errors.</li> <li>• Report loose connections, overheating, damaged components, or faulty trainer kits immediately to the lab instructor.</li> </ul>
8	Project Laboratory	<ul style="list-style-type: none"> <li>• Switch OFF the trainer kit or power supply before making circuit connections.</li> <li>• Verify all data bus, address bus, and interfacing connections carefully before powering ON.</li> <li>• Handle microprocessors, microcontrollers, and ICs carefully to avoid static damage.</li> <li>• Use the correct power supply voltage and avoid overloading the trainer kit.</li> <li>• Do not insert or remove ICs while the circuit is energized.</li> <li>• Use programming and debugging tools properly to prevent software or hardware errors.</li> <li>• Report loose connections, overheating, damaged components, or faulty trainer kits immediately to the lab instructor.</li> </ul>

### D3. Project Laboratory/Research Laboratory

**A. Availability of project laboratories/research laboratories**

- Ø Department has developed a project lab with internet facility exclusively for doing project works.
- Ø All the laboratories are utilized by the students for their project work.
- Ø 100Mbps internet connection is available in all the laboratory and Wi-Fi connection is provided in the entire campus.
- Ø Available software like MATLAB, CADENCE, Xilinx, Mentor Graphics, OrCAD, PSpice are used by students for project work.

**Table No. 7.5.1:** List of project laboratory/research laboratory /Centre of Excellence.

S.N.	Name of the Laboratory
1.	Project Laboratory
2.	Research Laboratory/ Texas Instruments Innovation Centre

**B. Utilization of project laboratories/research laboratory /Centre of excellence (05)**

The institution ensures that these laboratories is effectively utilized for academic and research purposes through:

1. Project-Based Learning:
  - o Students use these labs for final-year projects, mini-projects, and innovative research.
  - o Participation in national and international hackathons and technical competitions.
2. Industry-Oriented Training & Certifications:
  - o Hands-on training sessions on PLC programming, IoT applications, industrial automation, and real-time monitoring.
  - o Certification programs in collaboration with industry partners.
3. Research & Innovation Support:
  - o Research projects in automation, smart systems, and IoT applications.
  - o Faculty and students collaborate on funded research projects.
4. Skill Development & Employability Enhancement:
  - o Students gain practical exposure through lab-based learning and industry projects.
  - o Enhances job opportunities in automation, IoT, and industrial control systems.
5. Research Publications & Patents:
  - o Projects undertaken in these labs contribute to research publications and patents.

Collaboration with DST, TNSCST, AICTE, and other funding agencies for research grants.

**PART E: First Year faculty and financial Resources**

**(Data to be filled in for the first year course faculty and budget allocation and utilization)**

**E1. First Year Student-Faculty Ratio (FYSFR)**

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	540	27	19	14	67
2024-25(CAYm1)	570	28	23	14	76
2025-26(CAY)	390	20	19	10	86

**E2. Budget Allocation, Utilization, and Public Accounting at Institute Level**

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	2000000	1500000	150000	121000	2800000	2558604	3000000	2788366
Library	900000	859398	450000	424463	850000	778529	100000	913985
Laboratory equipment	3400000	3180000	15000000	13678000	16000000	14286790	3200000	2934513
Teaching and non-teaching staff salary	53000000	50292452	54000000	52233128	42000000	40915087	38000000	35103475
Outreach Programs	600000	564000	870000	830000	1100000	725000	120000	105000
R&D	1000000	860000	1000000	935000	2000000	1180000	120000	107290
Training, Placement and Industry linkage	1000000	953158	2000000	2568559	1700000	1440197	400000	388100
SDGs	600000	578000	370000	341000	800000	1180000	0	0
Entrepreneurship	200000	100000	150000	100000	150000	100000	200000	100000
Others, specify	28000000	22149633	22000000	19339735	25000000	22840149	25000000	20451537
<b>Total</b>	<b>90700000</b>	<b>81036641</b>	<b>95990000</b>	<b>90570885</b>	<b>92400000</b>	<b>86004356</b>	<b>70140000</b>	<b>62892266</b>

**E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level**

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	803000	450000	2000000	1888000	1000000	958750	150000	27650

Software	300000	230000	500000	250000	900000	880000	500000	280000
SDGs	200000	130000	100000	65000	200000	120000	0	0
Support for faculty development	200000	110000	200000	125000	200000	140000	0	70000
R & D	500000	320000	300000	180000	500000	210000	50000	30000
Industrial Training, Industry expert, Internship	200000	117000	250000	190000	140000	110000	200000	154100
Miscellaneous Expenses*	300000	222000	130000	92000	150000	90000	150000	101760
<b>Total</b>	<b>2503000</b>	<b>1579000</b>	<b>3480000</b>	<b>2790000</b>	<b>3090000</b>	<b>2508750</b>	<b>1050000</b>	<b>663510</b>